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## Voice Echo Canceller Block Diagram

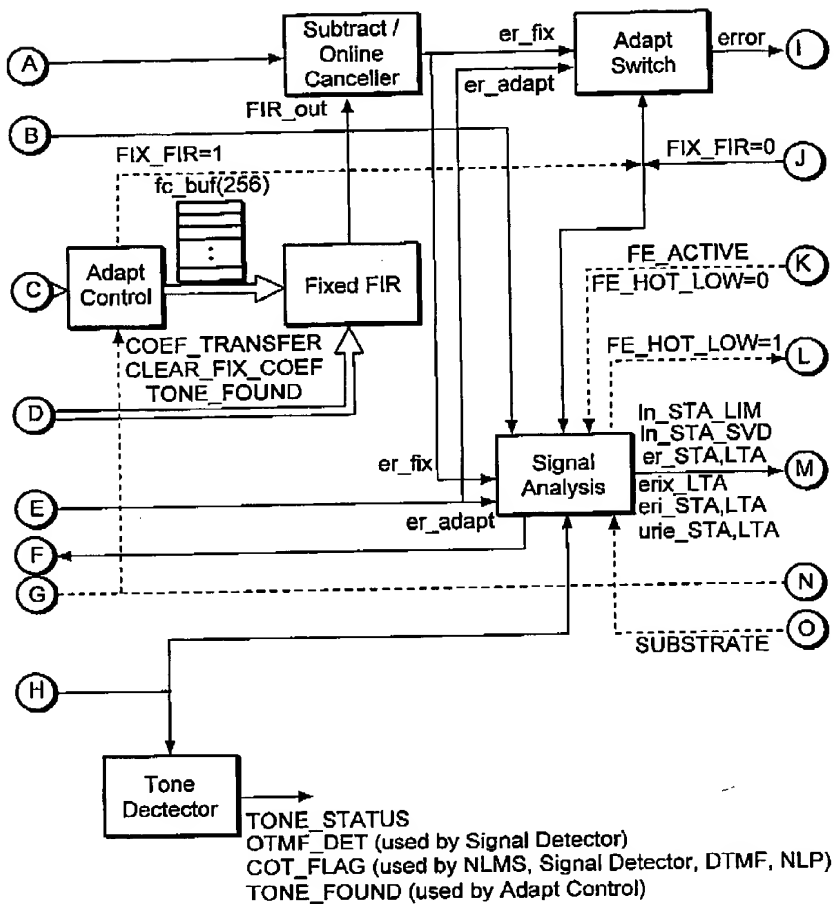


FIG. 1B

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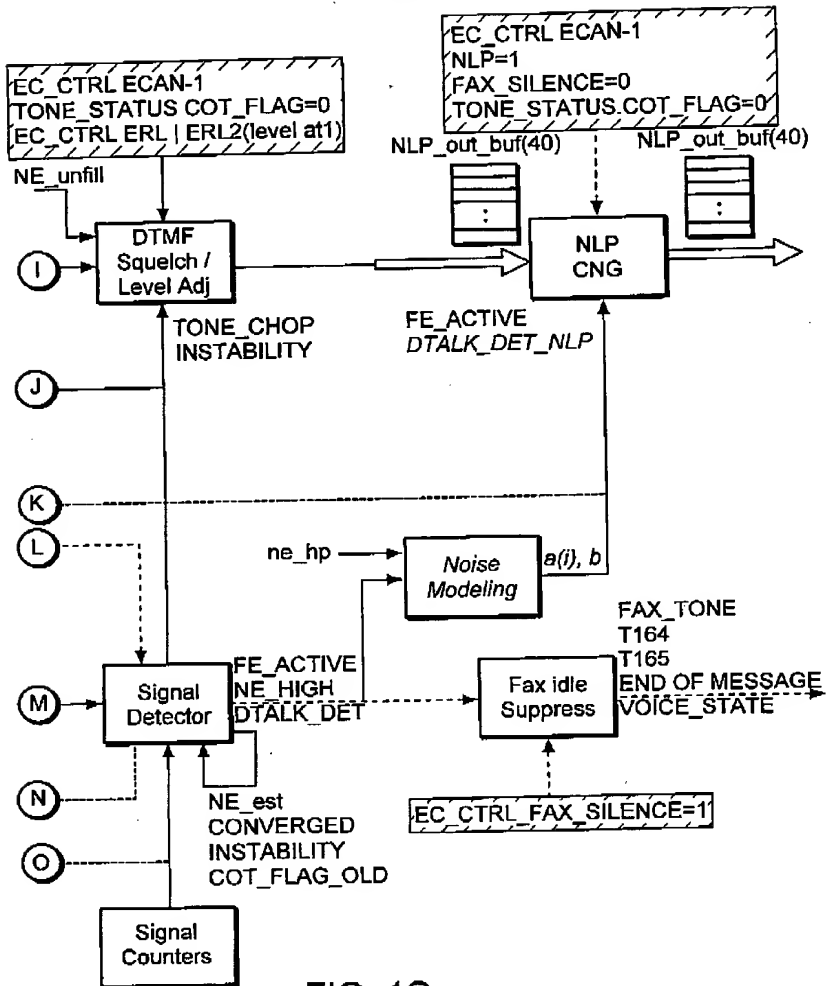


FIG. 1C

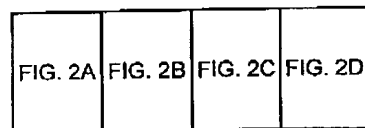


FIG. 2

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Run on every 40 samples: HighPass,  
NLPCNG  
Run on every 4 samples: Signal Analysis,  
Signal Detector, Noise Modeling  
Run on every 1 sample: All other blocks

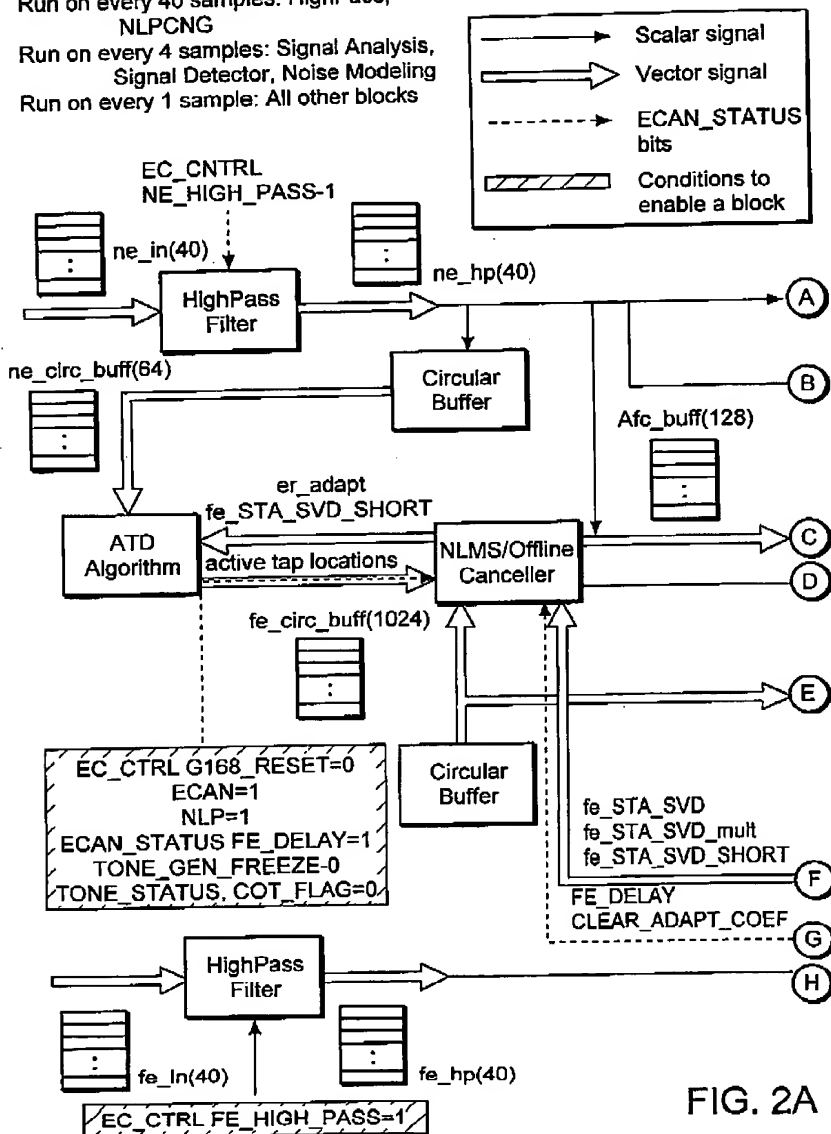


FIG. 2A

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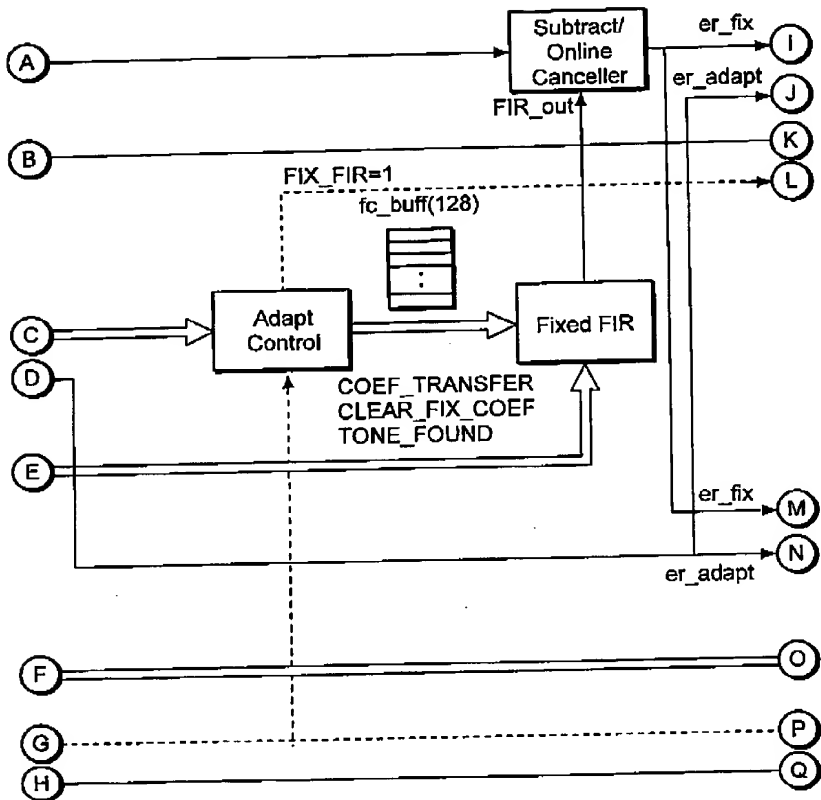
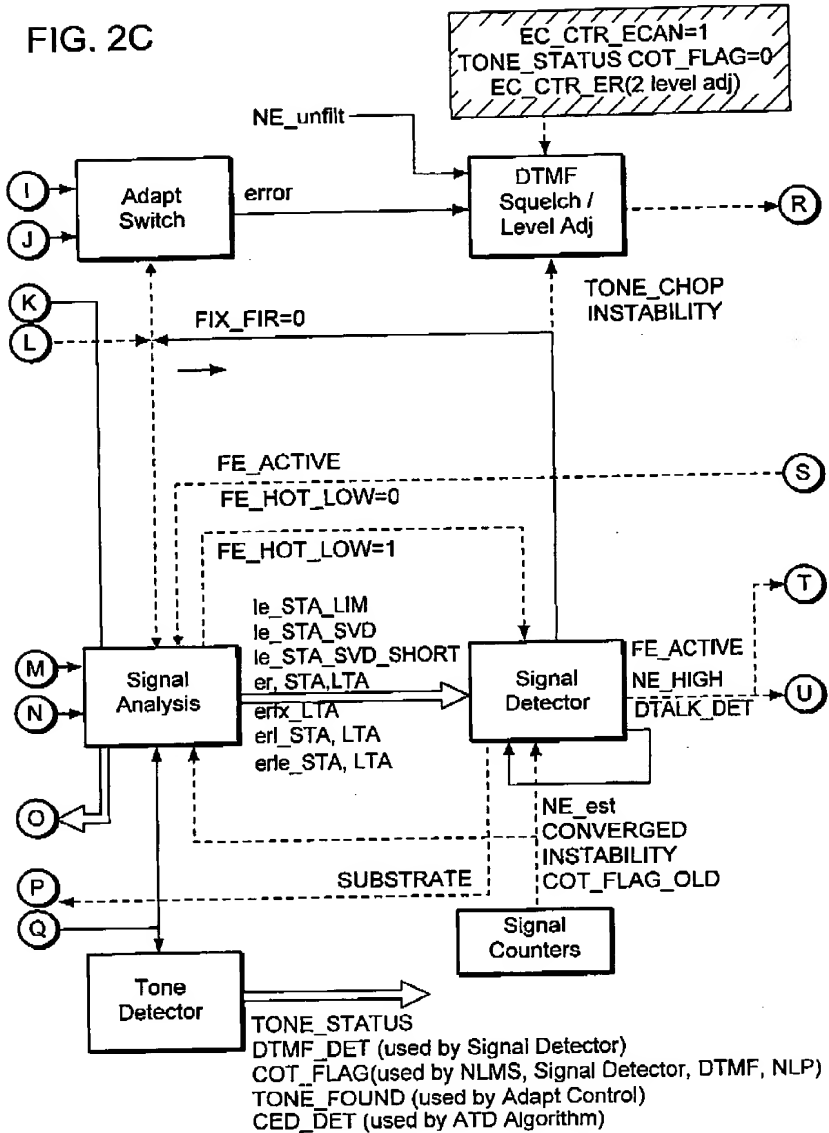
**Voice Echo Canceller Block Diagram with Active Tap Detection Algorithm**

FIG. 2B

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FIG. 2C



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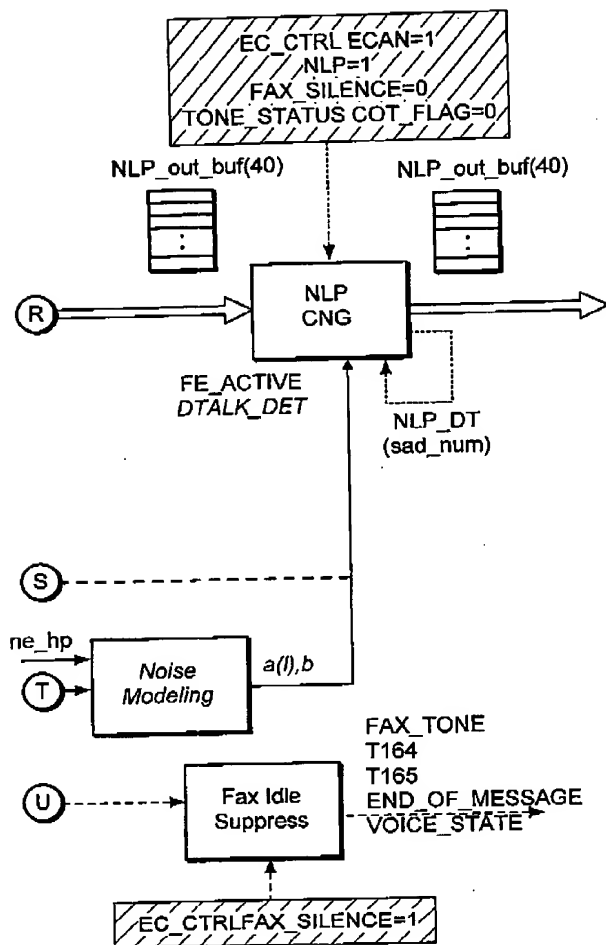


FIG. 2D

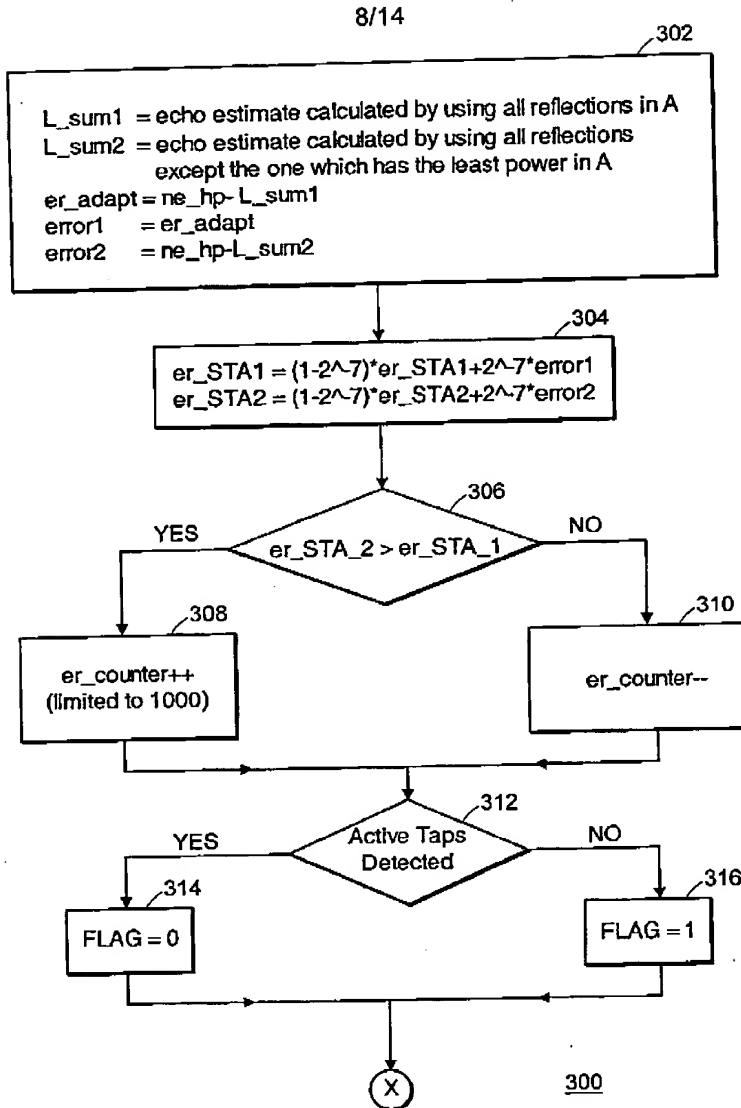


FIG. 3A



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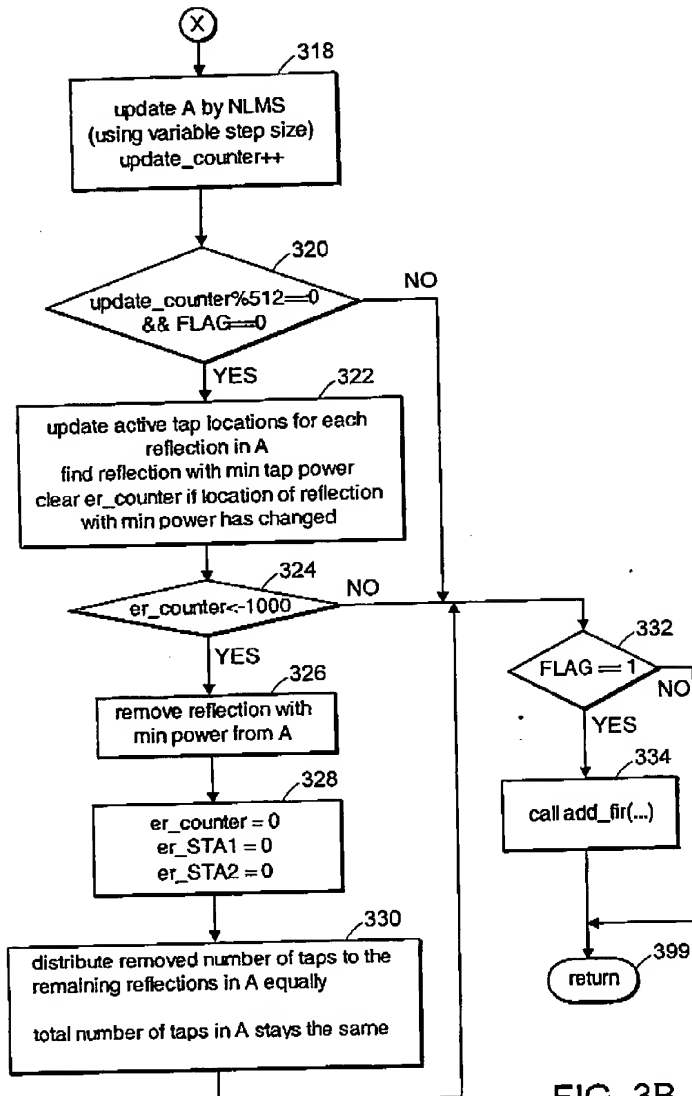


FIG. 3B

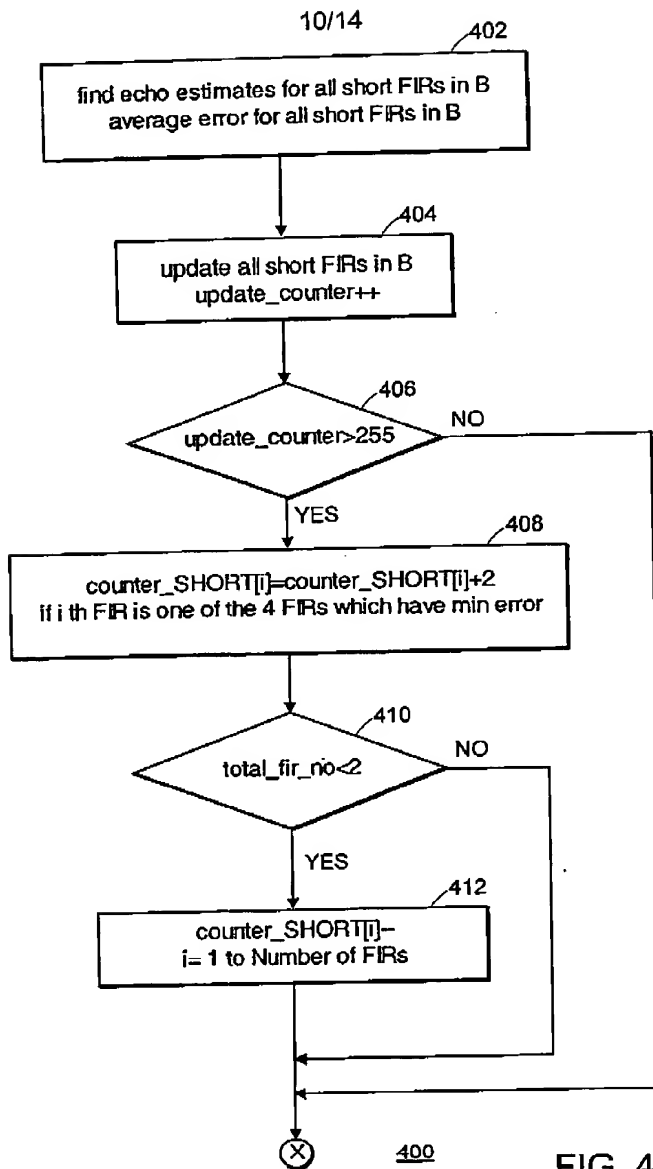


FIG. 4A

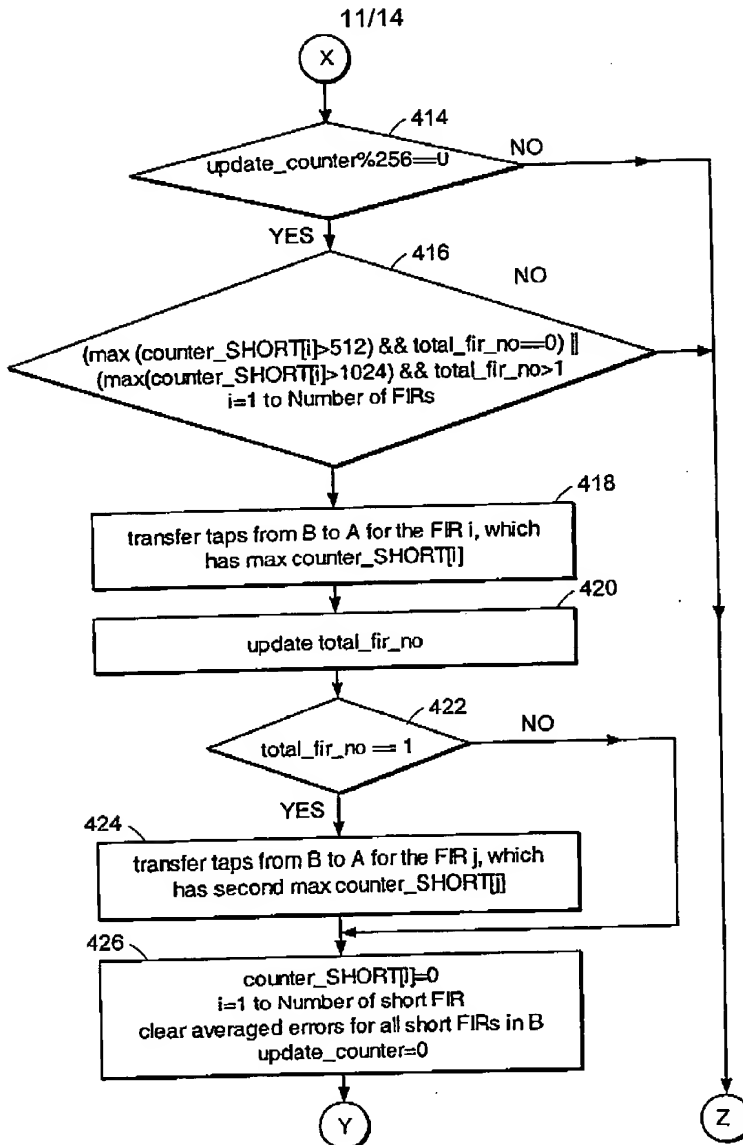


FIG. 4B

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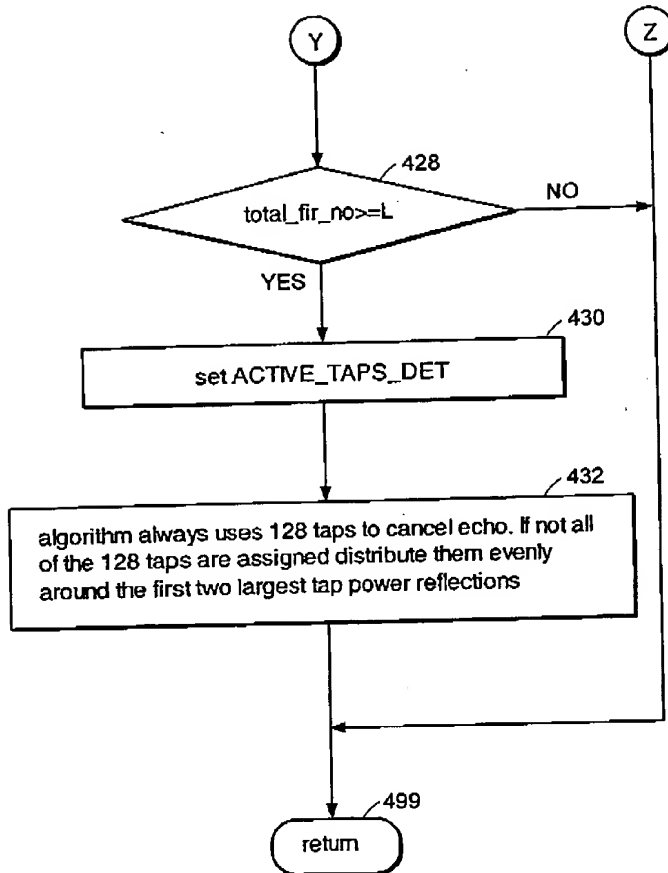
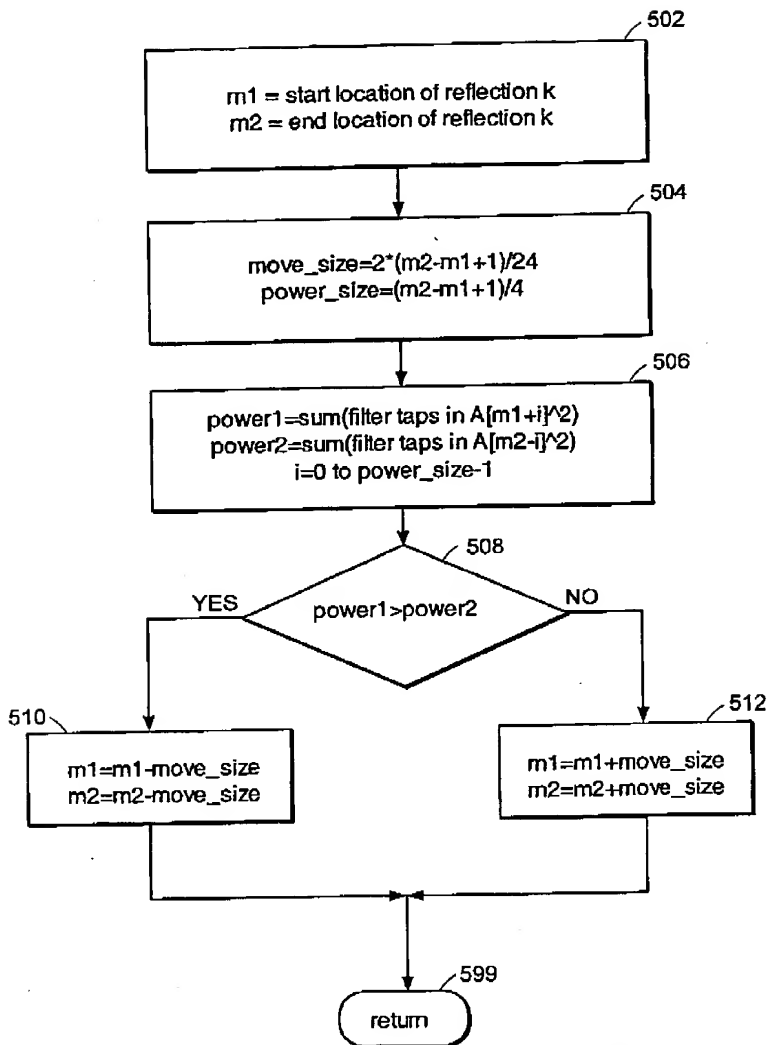


FIG. 4C

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FIG. 5

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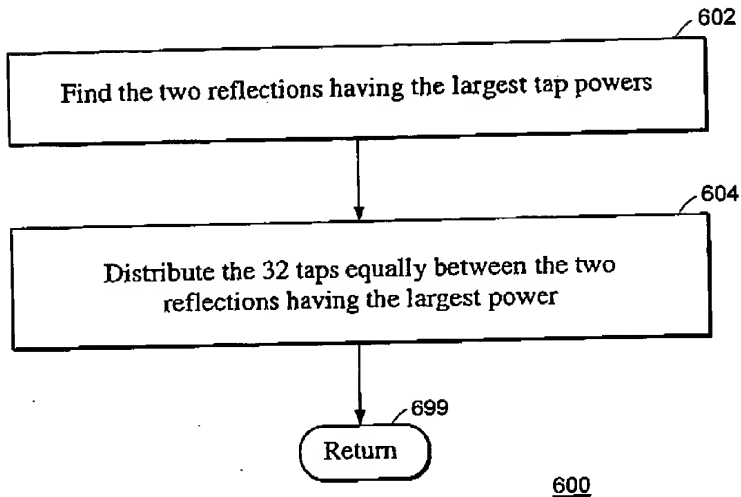


FIG. 6